

# SAT: **DISRUPTION**, **DEMISE** & **RESURGENCE**

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**Joao Marques-Silva**

**PoS Workshop**

**IST, Lisbon, Portugal**

**July 8 2019**



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**GRASP**  
The source of learning

# How good are CDCL SAT solvers?

Demos

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## Demos

- Sample SAT of solvers:
  1. **POSIT**: state of the art **DPLL** SAT solver in 1995
  2. **GRASP**: first **CDCL** SAT solver, state of the art 1995~2000
  3. **Minisat**: **CDCL** SAT solver, state of the art until the late 00s
  4. **Glucose**: modern state of the art **CDCL** SAT solver
  5. ...

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  5. ...
- **Example 1**: model checking example (from IBM)
- **Example 2**: cooperative path finding (CPF)

# How good are SAT solvers? – an example

- Cooperative pathfinding (CPF)
  - $N$  agents on some grid/graph
  - Start positions
  - Goal positions
  - Minimize makespan
  - Restricted planning problem

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- Concrete example
  - Gaming grid
  - 1039 vertices
  - 1928 edges
  - 100 agents



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```
*** tracker: a pathfinding tool ***
Initialization ... CPU Time: 0.004711
Number of variables: 113315
Tentative makespan 1
Number of variables: 226630
Number of assumptions: 1
c Running SAT solver ... CPU Time: 0.718112
c Done running SAT solver ... CPU Time: 0.830099
No solution for makespan 1
Elapsed CPU Time: 0.830112
Tentative makespan 2
Number of variables: 339945
Number of assumptions: 1
c Running SAT solver ... CPU Time: 1.27113
c Done running SAT solver ... CPU Time: 1.27114
No solution for makespan 2
Elapsed CPU Time: 1.27114
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Tentative makespan 24
Number of variables: 2832875
Number of assumptions: 1
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c Done running SAT solver ... CPU Time: 11.8653
No solution for makespan 24
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Tentative makespan 25
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c Done running SAT solver ... CPU Time: 16.6882
Solution found for makespan 25
Elapsed CPU Time: 16.6995
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- **Cooperative pathfinding (CPF)**
  - $N$  agents on some grid/graph
  - **Start** positions
  - **Goal** positions
  - Minimize **makespan**
  - Restricted planning problem
- Concrete example
  - Gaming grid
  - 1039 vertices
  - 1928 edges
  - 100 agents
  - **Formula w/ 2946190 variables!**
- **Note:** In the early 90s, SAT solvers could solve formulas **with a few hundred variables!**

```
*** tracker: a pathfinding tool ***

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  - **Obs:** SAT solvers in the late 90s (but formula dependent)
- Search space with 2832875 propositional variables (worst case):
  - # of assignments to  $> 2.8 \times 10^6$  variables:  $\gg 10^{840000}$  !!
  - **Obs:** SAT solvers at present (but formula dependent)

# 1 SAT Disruption



# The CDCL SAT disruption

- CDCL SAT solving is a **success story** of Computer Science

# The CDCL SAT disruption

- CDCL SAT solving is a **success story** of Computer Science
  - Conflict-Driven Clause Learning (CDCL)
  - (CDCL) SAT has impacted **many** different fields
  - Hundreds (thousands?) of practical applications



# So, what is a CDCL SAT solver?

- Extend DPLL SAT solver with:

[DP60, DLL62]

- Clause learning & non-chronological backtracking

[MS95, MSS96, MSS99]

- Search restarts

[GSC97, BMS00, Hua07, Bie08, LOM<sup>+</sup>18]

- Lazy data structures

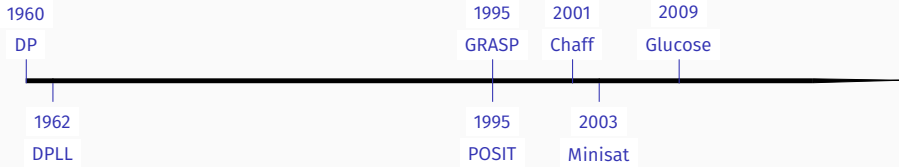
- Conflict-guided branching

- ...

# So, what is a CDCL SAT solver?

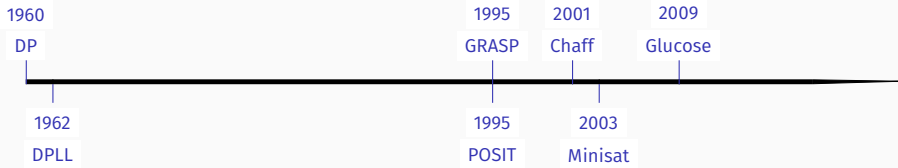
- Extend DPLL SAT solver with:
  - Clause learning & non-chronological backtracking [DP60, DLL62]
    - Exploit UIPs [MS95, MSS96, MSS99]
    - Minimize learned clauses [MS95, MSS99, ZMMM01, SSS12]
    - Opportunistically delete clauses [SB09, Gel09, LLX<sup>+</sup>17]
  - Search restarts [MSS96, MSS99, GN02, AS09]
  - Search restarts [GSC97, BMS00, Hua07, Bie08, LOM<sup>+</sup>18]
  - Lazy data structures
    - Watched literals [MMZ<sup>+</sup>01]
  - Conflict-guided branching
    - Lightweight branching heuristics [MMZ<sup>+</sup>01]
    - Phase saving [PD07]
  - ...

# CDCL timeline – somewhat incomplete



- DPLL (DP/DLL): backtracking search with unit propagation

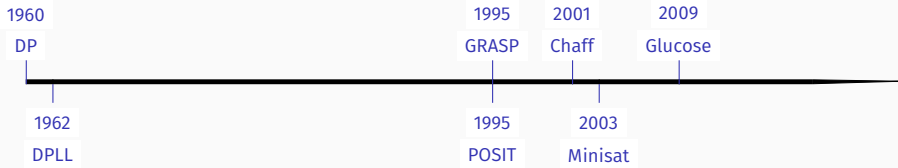
# CDCL timeline – somewhat incomplete



- DPLL (DP/DLL): backtracking search with unit propagation
- POSIT: efficient implementation of DPLL



# CDCL timeline – somewhat incomplete

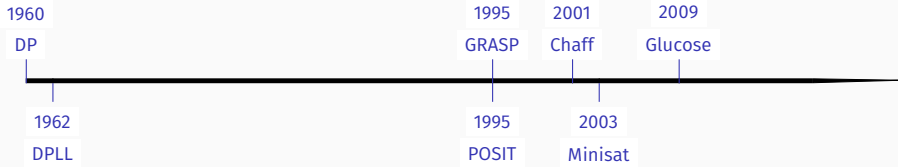


- DPLL (DP/DLL): backtracking search with unit propagation
- POSIT: efficient implementation of DPLL
- GRASP:
  1. Clause learning; UIPs, implication graphs, decision levels, antecedents, etc.
  2. Integration of search restarts with clause learning

[MS95, MSS96, MSS99]

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- Chaff:
  1. VSIDS, watched literals
  2. Always backtrack after conflict

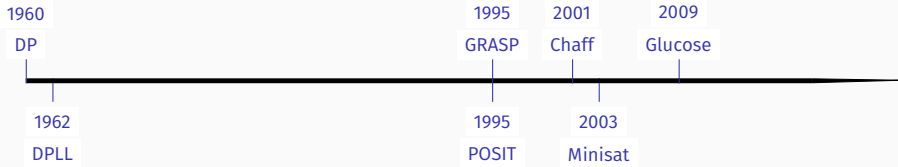
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- Minisat:
  1. Learned clause minimization

[MS95, MSS96, MSS99]

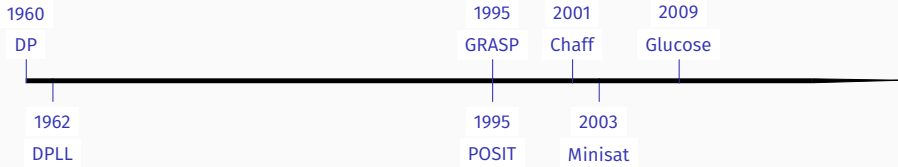
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- Glucose:
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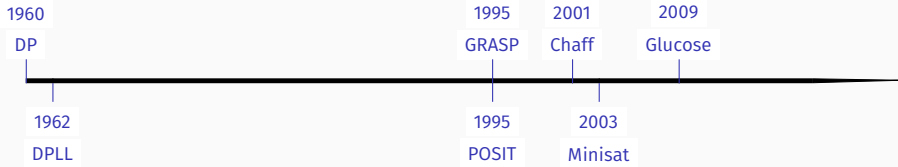
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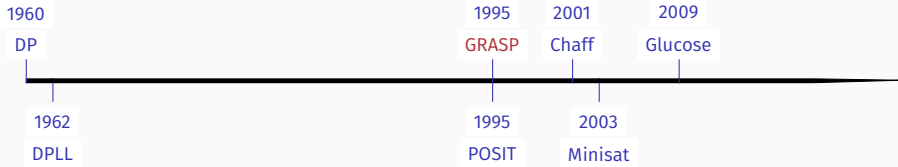
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[AS09]

## Efficient Generation of Test Patterns Using Boolean Difference

Tracy Larrabee  
Computer Science Department  
Stanford University  
Stanford, CA 94305

### Abstract

Most automatic test pattern generation systems for combinational circuits generate a test for a given fault by directly searching a data structure representing the circuit to be tested. This paper describes a new system that divides the problem into two parts: First, it constructs a formula expressing the Boolean difference between the unfaulted and faulted circuits. Second, it applies a **Boolean satisfiability algorithm** to the resulting formula. The new system can incorporate any of the heuristics used by structural search techniques. It is not only quite general, but is able to test or prove untestable every fault in the popular Brglez-Fujiwara[1] test benchmark.

ITC1989

## Timing Analysis and Delay-Fault Test Generation using Path-Recursive Functions\*

Patrick C. McGeer

Alexander Saldanha

Paul R. Stephan

Robert K. Brayton

Alberto L. Sangiovanni-Vincentelli

University of California - Berkeley CA

### Abstract

*Functional analysis of paths through combinational logic circuits has recently emerged as a critical problem in timing analysis and various forms of test generation. In this paper, we introduce an efficient method for generating the functional forms of path analysis problems. We demonstrate that the resulting function is linear in the size of the circuit. The functions are then tested for satisfiability either using a **Boolean network satisfiability algorithm** suggested in [5] or through the construction of BDD's [1]. The effectiveness of the proposed approach is shown for timing analysis and robust path delay-fault test generation. This method also holds promise for both static and dynamic hazard analysis, and for test generation using all other delay-fault models,  $\tau$ -irredundant fault models, and stuck-open fault models.*

ICCAD 1991



## Test Pattern Generation Using Boolean Satisfiability

Tracy Larrabee, *Member, IEEE*

**Abstract**—This article describes the Boolean satisfiability method for generating test patterns for single stuck-at faults in combinational circuits. This new method generates test patterns in two steps: First, it constructs a formula expressing the Boolean difference between the unfaulted and faulted circuits. Second, it applies a Boolean satisfiability algorithm to the resulting formula. This approach differs from previous methods now in use, which search the circuit structure directly instead of constructing a formula from it. The new method is general and effective: it allows for the addition of heuristics used by structural search methods, and it has produced excellent results on popular test pattern generation benchmarks.

IEEE TCAD 1992

## EECS 579: Digital System Testing

**Instructor:** [John P. Hayes](#)

### Coverage

This course examines the theory and practice of fault analysis, test generation, and design for testability for digital circuits and systems.

### Lab

A term project or paper is required which is tailored to individual student interests, and typically involve one of the following:

- A. Programming a test generation or simulation algorithm covered in the course
- B. In-depth literature survey of some advanced topic
- C. Individual research into some special topic or problem
- D. Experiments with commercial test and simulation CAD hardware or software

All projects require a written report and an oral presentation to the class at the end of the term. Item D is subject to availability of the hardware and software needed.

**Textbook(s)**



**UofM Spring'92**

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**Textbook(s)**



Larrabee's  
SAT algorithm  
didn't work!

UofM Spring'92

## Dynamic Search-Space Pruning Techniques in Path Sensitization

João P. Marques Silva and Karem A. Sakallah  
Department of Electrical Engineering and Computer Science  
University of Michigan


**Abstract**— A powerful combinational path sensitization engine is required for the efficient implementation of tools for test pattern generation, timing analysis, and delay fault testing. Path sensitization can be posed as a search, in the n-dimensional Boolean space, for a consistent assignment of logic values to the circuit nodes which also satisfies a given condition. In this paper we propose and demonstrate the effectiveness of several new techniques for search-space pruning for test pattern generation. In particular, we present linear-time algorithms for dynamically identifying unique sensitization points and for dynamically maintaining reduced head line sets. In addition, we present two powerful mechanisms that drastically reduce the number of backtracks: failure-driven assertions and dependency-directed backtracking. Both mechanisms can be viewed as a form of learning while searching and have analogs in other application domains. These search pruning methods have been implemented in a generic path sensitization engine called LEAP. A test pattern generator, TG-LEAP, that uses this engine was also developed. We present experimental results that compare the effectiveness of our proposed search pruning strategies to those of PODEM, FAN, and SOCRATES. In particular, we show that LEAP is very efficient in identifying undetectable faults and in generating tests for difficult faults.

DAC 1994

## Dynamic Search-Space Pruning Techniques in Path Sensitization

João P. Marques Silva and Karem A. Sakallah  
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**Abstract**— A powerful combinational path sensitization engine is required for the efficient implementation of tools for test pattern generation, timing analysis, and delay fault testing. Path sensitization can be posed as a search, in the n-dimensional Boolean space, for a consistent assignment of logic values to the circuit nodes which also satisfies a given condition. In this paper we propose and demonstrate the effectiveness of several new techniques for search-space pruning for test pattern generation. In particular, we present linear-time algorithms for dynamically identifying unique sensitization points and for dynamically maintaining reduced head line sets. In addition, we present two powerful mechanisms that drastically reduce the number of backtracks: failure-driven assertions and dependency-directed backtracking. Both mechanisms can be viewed as a form of learning while searching and have analogs in other application domains. These search pruning methods have been implemented in a generic path sensitization engine called LEAP. A test pattern generator, TG-LEAP, that uses this engine was also developed. We present experimental results that compare the effectiveness of our proposed search pruning strategies to those of PODEM, FAN, and SOCRATES. In particular, we show that LEAP is very efficient in identifying undetectable faults and in generating tests for difficult faults.



UIP's inspired  
on USP's!

DAC 1994

## SEARCH ALGORITHMS FOR SATISFIABILITY PROBLEMS IN COMBINATIONAL SWITCHING CIRCUITS

by

**João Paulo Marques da Silva**


A dissertation submitted in partial fulfillment  
of the requirements for the degree of  
Doctor of Philosophy  
(Electrical Engineering)  
in The University of Michigan  
1995

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1995



Proposes  
modern clause  
learning!

## IMPROVEMENTS TO PROPOSITIONAL SATISFIABILITY SEARCH ALGORITHMS

JON WILLIAM FREEMAN

A DISSERTATION

in

COMPUTER AND INFORMATION SCIENCE

Presented to the Faculties of the University of Pennsylvania in Partial Fulfillment of the  
Requirements for the Degree of Doctor of Philosophy.

1995



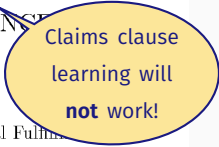
## IMPROVEMENTS TO PROPOSITIONAL SATISFIABILITY SEARCH ALGORITHMS

JON WILLIAM FREEMAN

A DISSERTATION

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COMPUTER AND INFORMATION SCIENCE



Claims clause  
learning will  
**not** work!

Presented to the Faculties of the University of Pennsylvania in Partial Fulfillment of the  
Requirements for the Degree of Doctor of Philosophy.

1995

## GRASP—A New Search Algorithm for Satisfiability

João P. Marques Silva  
Cadence European Laboratories  
IST/INESC  
1000 Lisboa, Portugal

Karem A. Sakallah  
Department of EECS  
University of Michigan  
Ann Arbor, Michigan 48109-2122

### Abstract

*This paper introduces GRASP (Generic seaRch Algorithm for the Satisfiability Problem), an integrated algorithmic framework for SAT that unifies several previously proposed search-pruning techniques and facilitates identification of additional ones. GRASP is premised on the inevitability of conflicts during search and its most distinguishing feature is the augmentation of basic backtracking search with a powerful conflict analysis procedure. Analyzing conflicts to determine their causes enables GRASP to backtrack non-chronologically to earlier levels in the search tree, potentially pruning large portions of the search space. In addition, by “recording” the causes of conflicts, GRASP can recognize and preempt the occurrence of similar conflicts later on in the search. Finally, straightforward bookkeeping of the causality chains leading up to conflicts allows GRASP to identify assignments that are necessary for a solution to be found. Experimental results obtained from a large number of benchmarks, including many from the field of test pattern generation, indicate that application of the proposed conflict analysis techniques to SAT algorithms can be extremely effective for a large number of representative classes of SAT instances.*

ICCAD 1996

## GRASP: A Search Algorithm for Propositional Satisfiability

João P. Marques-Silva, *Member, IEEE*, and Karem A. Sakallah, *Fellow, IEEE*

**Abstract**—This paper introduces GRASP (Generic seaRch Algorithm for the Satisfiability Problem), a new search algorithm for Propositional Satisfiability (SAT). GRASP incorporates several search-pruning techniques that proved to be quite powerful on a wide variety of SAT problems. Some of these techniques are specific to SAT, whereas others are similar in spirit to approaches in other fields of Artificial Intelligence. GRASP is premised on the inevitability of conflicts during the search and its most distinguishing feature is the augmentation of basic backtracking search with a powerful conflict analysis procedure. Analyzing conflicts to determine their causes enables GRASP to backtrack nonchronologically to earlier levels in the search tree, potentially pruning large portions of the search space. In addition, by “recording” the causes of conflicts, GRASP can recognize and preempt the occurrence of similar conflicts later on in the search. Finally, straightforward bookkeeping of the causality chains leading up to conflicts allows GRASP to identify assignments that are necessary for a solution to be found. Experimental results obtained from a large number of benchmarks indicate that application of the proposed conflict analysis techniques to SAT algorithms can be extremely effective for a large number of representative classes of SAT instances.

**Index Terms**—Satisfiability, search algorithms, conflict diagnosis, conflict-directed nonchronological backtracking, conflict-based equivalence, failure-driven assertions, unique implication points.



## Using Randomization and Learning to Solve Hard Real-World Instances of Satisfiability

CP 2000

Luís Baptista and João Marques-Silva

Department of Informatics, Technical University of Lisbon,  
IST/INESC/CEL, Lisbon, Portugal  
{lmtb, jpms}@algos.inesc.pt

**Abstract.** This paper addresses the interaction between randomization, with restart strategies, and learning, an often crucial technique for proving unsatisfiability. We use instances of SAT from the hardware verification domain to provide evidence that randomization can indeed be essential in solving real-world satisfiable instances of SAT. More interestingly, our results indicate that randomized restarts and learning may cooperate in proving both satisfiability and unsatisfiability. Finally, we utilize and expand the idea of algorithm portfolio design to propose an alternative approach for solving hard unsatisfiable instances of SAT.

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## Using CSP Look-Back Techniques to Solve Real-World SAT Instances

**Roberto J. Bayardo Jr.**

The University of Texas at Austin  
Department of Computer Sciences (C0500)  
Austin, TX 78712 USA  
bayardo@cs.utexas.edu  
<http://www.cs.utexas.edu/users/bayardo>

**Robert C. Schrag**

Information Extraction and Transport, Inc.  
1730 North Lynn Street, Suite 502  
Arlington, VA 22209 USA  
schrag@iet.com  
<http://www.iet.com/users/schrag>

### Abstract

We report on the performance of an enhanced version of the “Davis-Putnam” (DP) proof procedure for propositional satisfiability (SAT) on large instances derived from real-world problems in planning, scheduling, and circuit diagnosis and synthesis. Our results show that incorporating CSP look-back techniques -- especially the relatively new technique of relevance-bounded learning -- renders easy many problems which otherwise are beyond DP’s reach. Frequently they make DP, a systematic algorithm, perform as well or better than stochastic SAT algorithms such as GSAT or WSAT. We recommend that such techniques be included as options in implementations of DP, just as they are in systematic algorithms for the more general constraint satisfaction problem.

**Different learning: no UIPs,  
no decision levels, etc.**

**AAAI 1997**

## SATO: An Efficient Propositional Prover

CADE 1997

Hantao Zhang\*

Department of Computer Science  
The University of Iowa  
Iowa City, IA 52242-1419, USA  
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Rehash of GRASP,  
without UIPs

SATO (Satisfiability Testing Optimized) is a propositional prover based on the Davis-Putnam method [3], which is one of the major practical methods for the satisfiability (SAT) problem of propositional logic. The first report of SATO appeared in [12]. Since then, we constantly add new techniques into SATO to make it more efficient [14, 13].

## Symbolic Model Checking without BDDs<sup>\*</sup>

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**Abstract.** Symbolic Model Checking [3, 14] has proven to be a powerful technique for the verification of reactive systems. BDDs [2] have traditionally been used as a symbolic representation of the system. In this paper we show how boolean decision procedures, like Stålmarck's Method [16] or the Davis & Putnam Procedure [7], can replace BDDs. This new technique avoids the space blow up of BDDs, generates counterexamples much faster, and sometimes speeds up the verification. In addition, it produces counterexamples of minimal length. We introduce a *bounded model checking* procedure for LTL which reduces model checking to propositional satisfiability. We show that bounded LTL model checking can be done without a tableau construction. We have implemented a model checker **BMC**, based on bounded model checking, and preliminary results are presented.

TACAS 1999

## 33.1

### Chaff: Engineering an Efficient SAT Solver

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#### ABSTRACT

Boolean Satisfiability is probably the most studied of combinatorial optimization/search problems. Significant effort has been devoted to trying to provide practical solutions to this problem for problem instances encountered in a range of applications in Electronic Design Automation (EDA), as well as in Artificial Intelligence (AI). This study has culminated in the development of several SAT packages, both proprietary and in the public domain (e.g. GRASP, SATO) which find significant use in both research and industry. Most existing complete solvers are variants of the Davis-Putnam (DP) search algorithm. In this paper we describe the development of a new complete solver, Chaff, which achieves significant performance gains through careful engineering of all aspects of the search – especially a particularly efficient implementation of Boolean constraint propagation (BCP) and a novel low overhead decision strategy. Chaff has been able to obtain one to two orders of magnitude performance improvement on difficult SAT benchmarks in comparison with other solvers (DP or otherwise), including GRASP and SATO.

Chaff showed how to make GRASP-like clause learning scale in practice!

DAC 2001



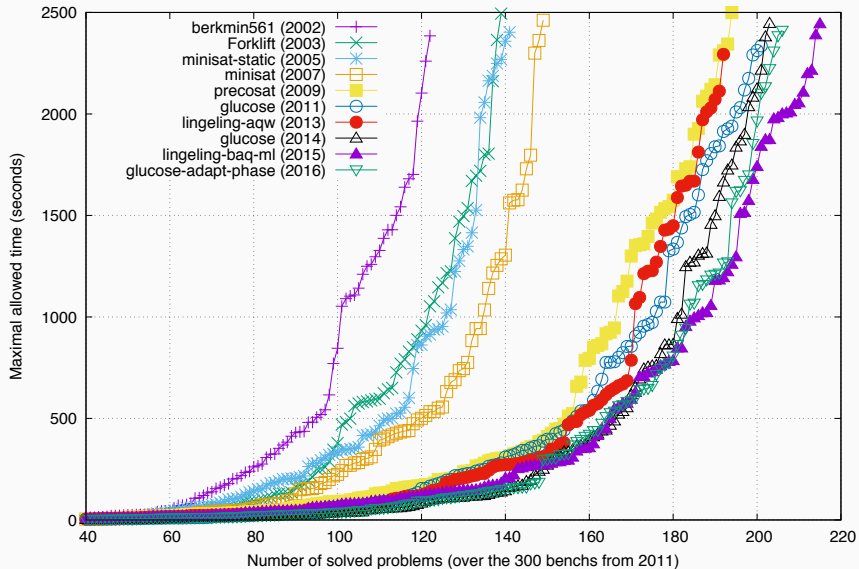
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# SAT Demise?



# CDCL SAT solver (continued?) improvement

[Source: Simon 2015]



## Is there a problem with SAT?

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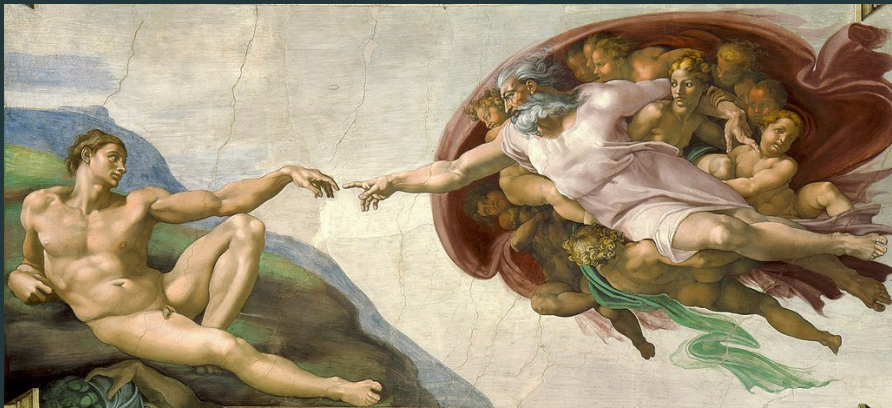
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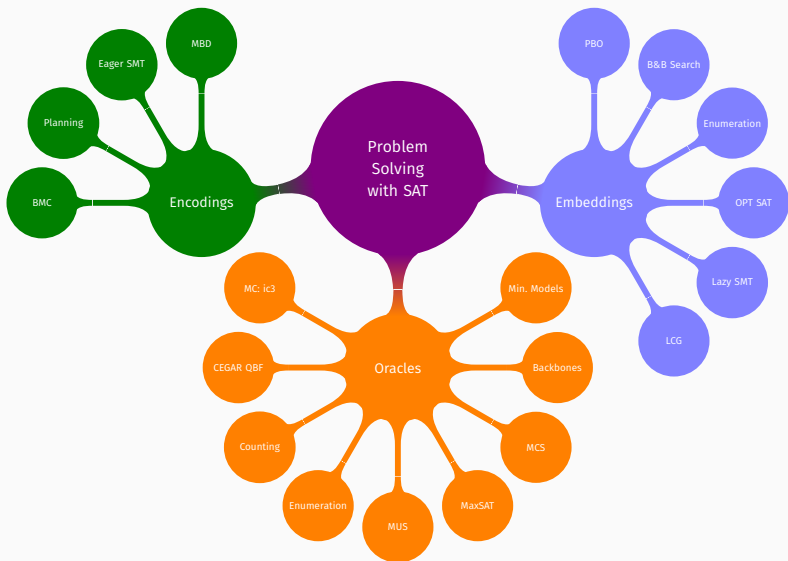
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- General perception among **some** researchers ...
- **Q: Is there a point in SAT research at present?**

# 3 SAT Resurgence

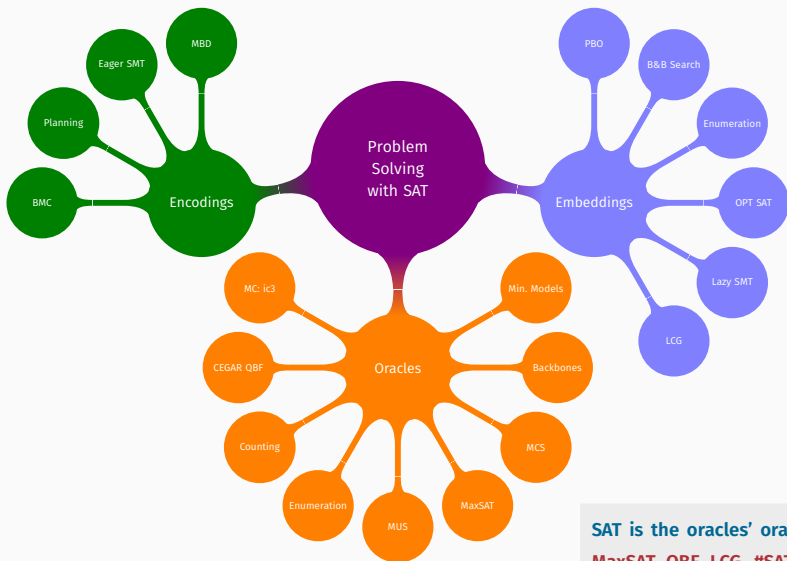




# CDCL SAT is ubiquitous in problem solving



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**SAT is the oracles' oracle:**

**MaxSAT, QBF, LCG, #SAT, SMT,  
ASP, FOL, ...**

# Age of SAT-enabled modular reasoning

MSMP  
MaxSAT  
QBF, MLK  
#SAT  
DQBF

SAT-  
Enabled  
Modular  
Reasoning

SMT  
CP  
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FOL

# Age of SAT-enabled modular reasoning

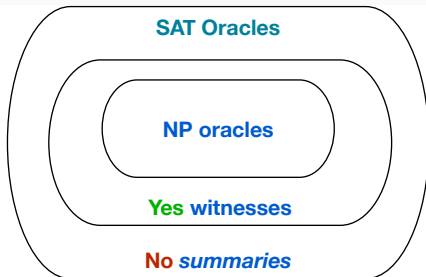
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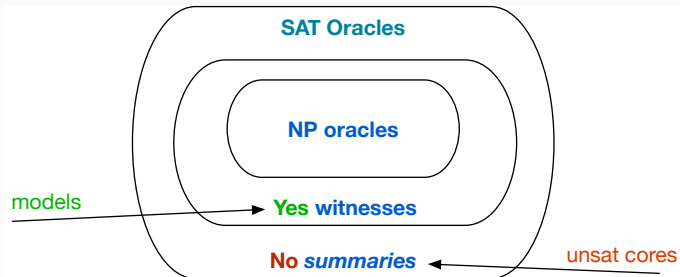
SMT  
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SAT is a  
**key** enabler  
technology

## So what are **SAT** oracles?



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# The power of the (SAT) oracle

- **Q:** How to solve the **FSAT** problem?

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  3. Call NP oracle on  $\mathcal{F}_i \wedge (x_i)$
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- **Note:** FSAT can be solved with **one** SAT oracle call

# Beyond decision problems

Answer

Problem Type

---

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Yes/No

Decision Problems

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Answer

Problem Type

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Decision Problems

**Some solution**

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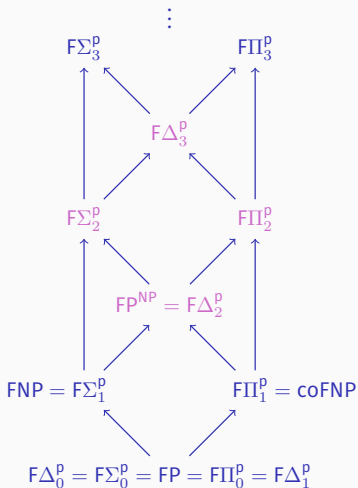
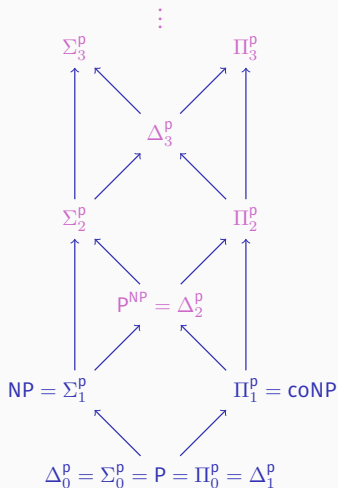
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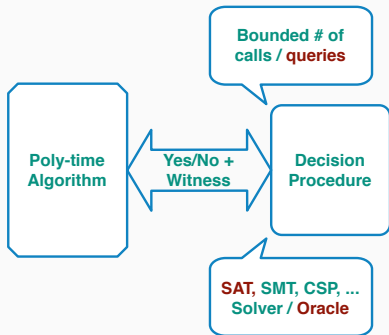
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<b># solutions</b>	Counting Problems

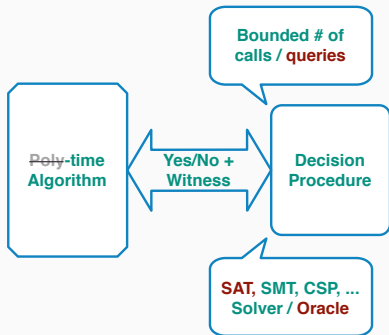
# ... and beyond NP – decision and function problems



# Oracle-based problem solving – simple scenario



# Oracle-based problem solving – general setting



# Many problems to solve – within $FP^{NP}$

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## Function Problems on Propositional Formulas

MaxSAT      PBO      ...      WBO      MinSAT

Minimal Models      Prime Implicants

Maximal Models      Autarkies

Backbones      Prime Implicates

MUSes      MCSes      ...      MESes      Indep. Vars

MFses      MSSes      MDSes      Implicant Ext.

MNSes      Implicate Ext.

MCFses



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## Function Problems on Propositional Formulas

### Optimization Problems

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PBO

...

WBO

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### Minimal Sets

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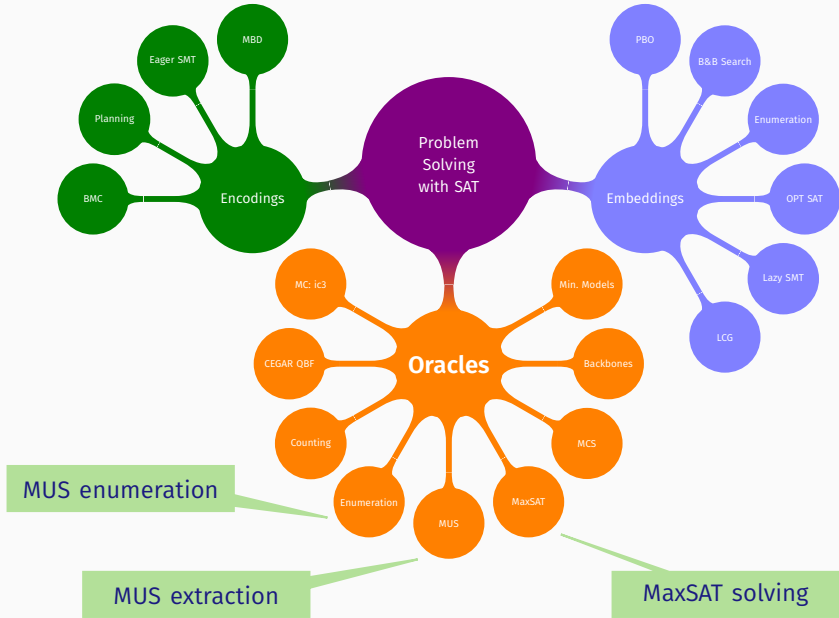
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# Selection of topics



Minimal Unsatisfiability

MUS Enumeration

Maximum Satisfiability

## Analyzing inconsistency – timetabling

Subject	Day	Time	Room
Intro Prog	Mon	9:00-10:00	6.2.46
Intro AI	Tue	10:00-11:00	8.2.37
Databases	Tue	11:00-12:00	8.2.37
... (hundreds of consistent constraints)			
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**Minimality**  
matters!

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- Given  $\mathcal{F} (\models \perp)$ ,  $\mathcal{M} \subseteq \mathcal{F}$  is a **Minimal Unsatisfiable Subset (MUS)** iff  $\mathcal{M} \models \perp$  and  $\forall \mathcal{M}' \subsetneq \mathcal{M}, \mathcal{M}' \not\models \perp$

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- MUSes and MCSes are (subset-)minimal sets
- MUSes and minimal hitting sets of MCSes and vice-versa
  - Easy to see **why**

[Rei87, BS05]



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- MUSes and MCSes are (subset-)minimal sets
- MUSes and minimal hitting sets of MCSes and vice-versa [Rei87, BS05]
  - Easy to see **why**
- How to compute MUSes & MCSes **efficiently** with SAT oracles?

# Why it matters?

- Analysis of over-constrained systems

- Model-based diagnosis

- Software fault localization
    - Spreadsheet debugging
    - Debugging relational specifications (e.g. Alloy)
    - Type error debugging
    - Axiom pinpointing in description logics
    - ...

[Rei87]

- Model checking of software & hardware systems
  - Inconsistency measurement
  - Minimal models; MinCost SAT; ...
  - ...

- Find minimal relaxations to recover consistency

- But also minimum relaxations to recover consistency, eg. MaxSAT

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Enumeration  
required!

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# Deletion-based algorithm

**Input** : Set  $\mathcal{F}$

**Output:** Minimal subset  $\mathcal{M}$

**begin**

$\mathcal{M} \leftarrow \mathcal{F}$

**foreach**  $c \in \mathcal{M}$  **do**

**if**  $\neg\text{SAT}(\mathcal{M} \setminus \{c\})$  **then**

$\mathcal{M} \leftarrow \mathcal{M} \setminus \{c\}$

**return**  $\mathcal{M}$

// If  $\neg\text{SAT}(\mathcal{M} \setminus \{c\})$ , then  $c \notin \text{MUS}$

// Final  $\mathcal{M}$  is MUS

**end**

- Number of oracles calls:  $\mathcal{O}(m)$

[CD91, BDTW93]

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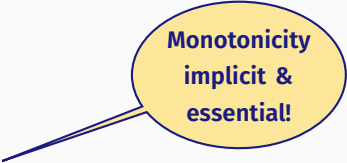
**foreach**  $c \in \mathcal{M}$  **do**

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**return**  $\mathcal{M}$

**end**



Monotonicity  
implicit &  
essential!

// Remove  $c$  from  $\mathcal{M}$

// Final  $\mathcal{M}$  is MUS

- Number of oracles calls:  $\mathcal{O}(m)$

[CD91, BDTW93]

## Deletion – MUS example

$c_1$	$c_2$	$c_3$	$c_4$	$c_5$	$c_6$	$c_7$
$(\neg x_1 \vee \neg x_2)$	$(x_1)$	$(x_2)$	$(\neg x_3 \vee \neg x_4)$	$(x_3)$	$(x_4)$	$(x_5 \vee x_6)$

$\mathcal{M}$	$\mathcal{M} \setminus \{c\}$	$\neg \text{SAT}(\mathcal{M} \setminus \{c\})$	Outcome
---------------	-------------------------------	--	---------

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$c_1..c_7$	$c_2..c_7$	1	Drop $c_1$

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$c_4..c_7$	$c_5..c_7$	0	Keep $c_4$

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$c_4..c_7$	$c_5..c_7$	0	Keep $c_4$
$c_4..c_7$	$c_4c_6c_7$	0	Keep $c_5$

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- MUS:  $\{c_4, c_5, c_6\}$

# Many MUS algorithms

- Formula  $\mathcal{F}$  with  $m$  clauses  $k$  the size of largest minimal subset

Algorithm	Oracle Calls	Reference
Insertion-based	$\mathcal{O}(km)$	[dSNP88, vMW08]
MCS_MUS	$\mathcal{O}(km)$	[BK15]
Deletion-based	$\mathcal{O}(m)$	[CD91, BDTW93]
Linear insertion	$\mathcal{O}(m)$	[MSL11, BLM12]
Dichotomic	$\mathcal{O}(k \log(m))$	[HLSB06]
QuickXplain	$\mathcal{O}(k + k \log(\frac{m}{k}))$	[Jun04]
Progression	$\mathcal{O}(k \log(1 + \frac{m}{k}))$	[MJB13]

- Note:** Lower bound in  $\text{FP}_{||}^{\text{NP}}$  and upper bound in  $\text{FP}^{\text{NP}}$  [CT95]
- Oracle calls correspond to testing **unsatisfiability** with SAT solver
- Practical optimizations: **clause set trimming**; **clause set refinement**; **redundancy removal**; (recursive) model rotation

Minimal Unsatisfiability

**MUS Enumeration**

Maximum Satisfiability



# How to enumerate MUSes?

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## 1. Standard solution:

Exploit HS duality between MCSes and MUSes

[Rei87, LS08]

MCSes are MHSES of MUSes and vice-versa

- Enumerate **all** MCSes and then enumerate **all** MHSES of the MCSes, i.e. **compute all the MUSes**
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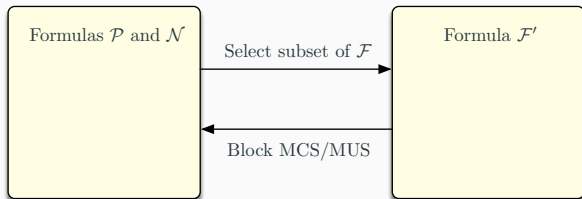
## 2. Exploit recent advances in 2QBF solving

## 3. Implicit hitting set dualization

[LPMM16]

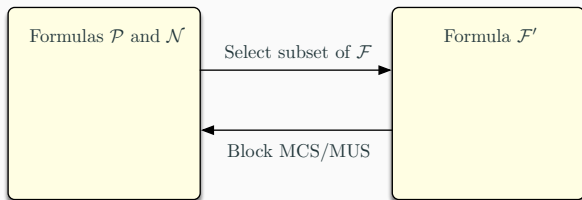
- Most effective if MUSes provided to user **on-demand**
- Also used in **prime enumeration, propositional abduction, logic synthesis, SMUS, quantification & XAI**

# How to enumerate MUSes, preferably?



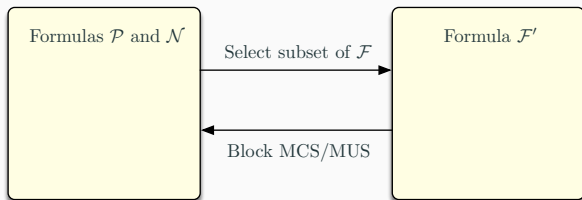
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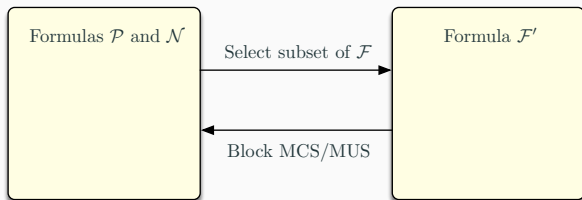
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  - **Must not** repeat **MUSes**
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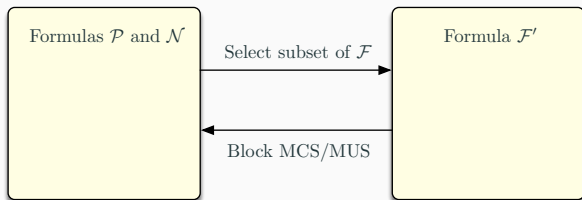
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5. Repeat loop

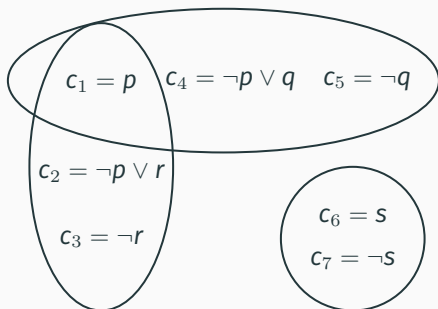
# MARCO/eMUS algorithm

**Input:** CNF formula  $\mathcal{F}$

```
1 begin
2    $I \leftarrow \{p_i \mid c_i \in \mathcal{F}\}$ 
3    $(\mathcal{P}, \mathcal{N}) \leftarrow (\emptyset, \emptyset)$ 
4   while true do
5      $(st, H) \leftarrow \text{MinHittingSet}(\mathcal{N}, \mathcal{P})$ 
6     if not st then return
7      $\mathcal{F}' \leftarrow \{c_i \mid p_i \in I \wedge p_i \notin H\}$ 
8     if not SAT( $\mathcal{F}'$ ) then
9        $\mathcal{M} \leftarrow \text{ComputeMUS}(\mathcal{F}')$ 
10      ReportMUS( $\mathcal{M}$ )
11       $\mathcal{N} \leftarrow \mathcal{N} \cup \{\neg p_i \mid c_i \in \mathcal{M}\}$ 
12    else
13       $\mathcal{P} \leftarrow \mathcal{P} \cup \{p_i \mid p_i \in H\}$ 
14 end
```

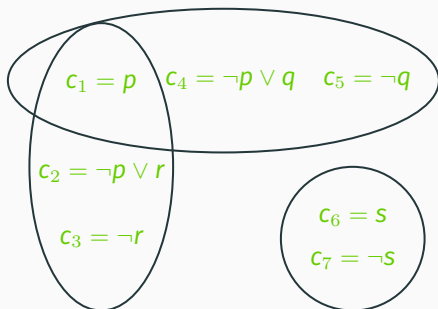
# An example

MinHS ( $\mathcal{N}$ )	$\mathcal{F}'$	MUS/MCS
$p_1 p_2 p_3 p_4 p_5 p_6 p_7$	S/U	
1111111	U	$\neg p_1 \vee \neg p_2 \vee \neg p_3$
0111111	U	$\neg p_6 \vee \neg p_7$
0111101	S	$p_1 \vee p_6$
1011101	U	$\neg p_1 \vee \neg p_4 \vee \neg p_5$
1101010	S	$p_3 \vee p_5 \vee p_7$
1010110	S	$p_2 \vee p_4 \vee p_7$
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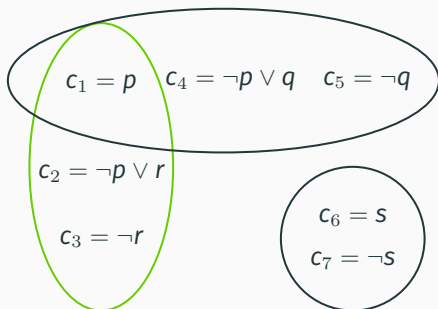
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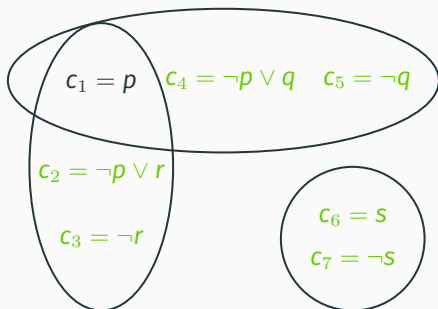
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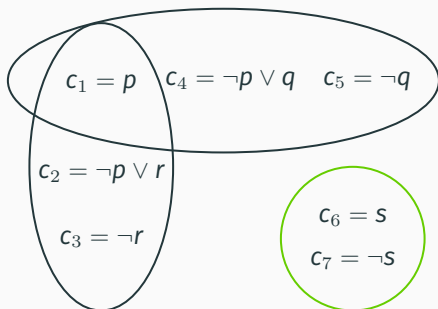
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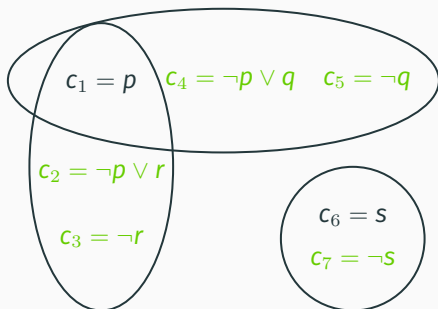
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1011010	S	$p_2 \vee p_5 \vee p_7$



# An example

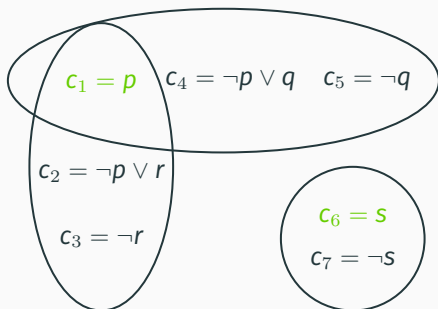
MinHS ( $\mathcal{N}$ )	$\mathcal{F}'$	MUS/MCS
$p_1 p_2 p_3 p_4 p_5 p_6 p_7$	S/U	
1111111	U	$\neg p_1 \vee \neg p_2 \vee \neg p_3$
0111111	U	$\neg p_6 \vee \neg p_7$
0111101	S	$p_1 \vee p_6$
1011101	U	$\neg p_1 \vee \neg p_4 \vee \neg p_5$
1101010	S	$p_3 \vee p_5 \vee p_7$
1010110	S	$p_2 \vee p_4 \vee p_7$
1100101	S	$p_3 \vee p_4 \vee p_6$
0111110	S	$p_1 \vee p_7$
1101001	S	$p_3 \vee p_5 \vee p_6$
1010101	S	$p_2 \vee p_4 \vee p_6$
1011001	S	$p_2 \vee p_5 \vee p_6$
1100110	S	$p_3 \vee p_4 \vee p_7$
1011010	S	$p_2 \vee p_5 \vee p_7$





# An example

MinHS ( $\mathcal{N}$ )	$\mathcal{F}'$	MUS/MCS
$p_1 p_2 p_3 p_4 p_5 p_6 p_7$	S/U	
1111111	U	$\neg p_1 \vee \neg p_2 \vee \neg p_3$
0111111	U	$\neg p_6 \vee \neg p_7$
0111101	S	$p_1 \vee p_6$
1011101	U	$\neg p_1 \vee \neg p_4 \vee \neg p_5$
1101010	S	$p_3 \vee p_5 \vee p_7$
1010110	S	$p_2 \vee p_4 \vee p_7$
1100101	S	$p_3 \vee p_4 \vee p_6$
0111110	S	$p_1 \vee p_7$
1101001	S	$p_3 \vee p_5 \vee p_6$
1010101	S	$p_2 \vee p_4 \vee p_6$
1011001	S	$p_2 \vee p_5 \vee p_6$
1100110	S	$p_3 \vee p_4 \vee p_7$
1011010	S	$p_2 \vee p_5 \vee p_7$



Minimal Unsatisfiability

MUS Enumeration

Maximum Satisfiability

# Recap MaxSAT

$x_6 \vee x_2$	$\neg x_6 \vee x_2$	$\neg x_2 \vee x_1$	$\neg x_1$
$\neg x_6 \vee x_8$	$x_6 \vee \neg x_8$	$x_2 \vee x_4$	$\neg x_4 \vee x_5$
$x_7 \vee x_5$	$\neg x_7 \vee x_5$	$\neg x_5 \vee x_3$	$\neg x_3$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable

# Recap MaxSAT

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable
- A **Minimal Correction Subset (MCS)** is an irreducible relaxation of the formula

# Recap MaxSAT

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable
- A **Minimal Correction Subset (MCS)** is an irreducible relaxation of the formula
- The MaxSAT solution is one of the **smallest** MCSes

# Recap MaxSAT

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable
- A **Minimal Correction Subset (MCS)** is an irreducible relaxation of the formula
- The MaxSAT solution is one of the **smallest** MCSes
  - **Note:** Clauses can have weights & there can be hard clauses

# Recap MaxSAT

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable
- A **Minimal Correction Subset (MCS)** is an irreducible relaxation of the formula
- The MaxSAT solution is one of the **smallest cost** MCSes
  - **Note:** Clauses can have weights & there can be hard clauses

# Recap MaxSAT

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

- Given **unsatisfiable** formula, find **largest** subset of clauses that is satisfiable
- A **Minimal Correction Subset (MCS)** is an irreducible relaxation of the formula
- The MaxSAT solution is one of the **smallest cost** MCSes
  - **Note:** Clauses can have weights & there can be hard clauses
- **Many** practical applications



# MaxSAT problem(s)

		Hard Clauses?	
		No	Yes
Weights?	No		
	Yes		

# MaxSAT problem(s)

		Hard Clauses?	
		No	Yes
Weights?	No	Plain	Partial
	Yes	Weighted	Weighted Partial

# MaxSAT problem(s)

		Hard Clauses?	
		No	Yes
Weights?	No	Plain	Partial
	Yes	Weighted	Weighted Partial

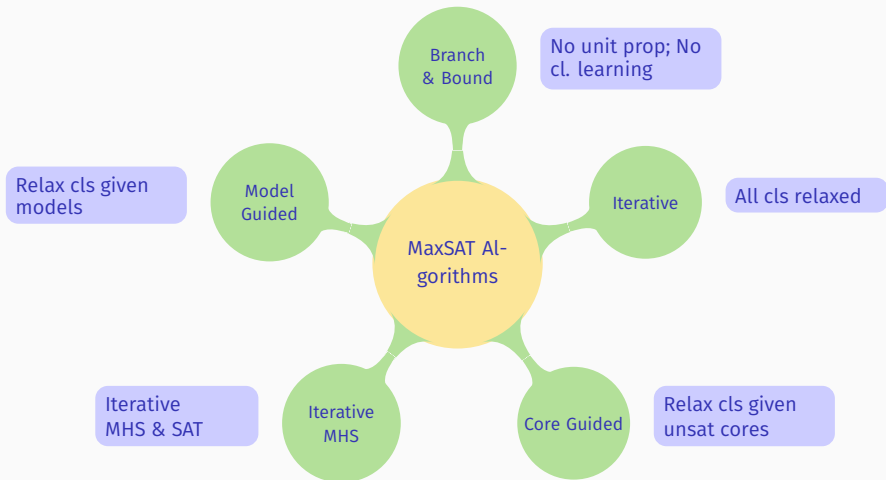
- **Must** satisfy **hard** clauses, if any
- Compute set of satisfied **soft** clauses with **maximum cost**
  - Without weights, cost of each falsified soft clause is 1
- **Or**, compute set of falsified **soft** clauses with **minimum cost** (s.t. **hard** & remaining **soft** clauses are satisfied)

# MaxSAT problem(s)

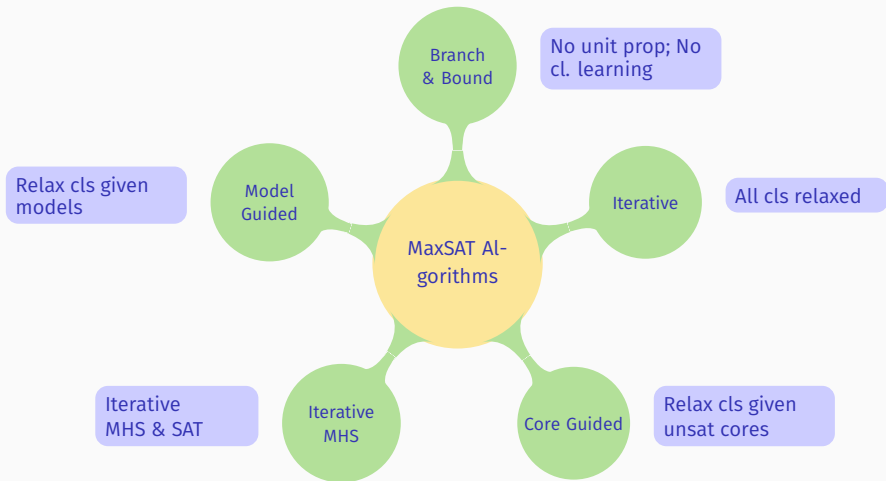
		Hard Clauses?	
		No	Yes
Weights?	No	Plain	Partial
	Yes	Weighted	Weighted Partial

- **Must** satisfy **hard** clauses, if any
- Compute set of satisfied **soft** clauses with **maximum cost**
  - Without weights, cost of each falsified soft clause is 1
- **Or**, compute set of falsified **soft** clauses with **minimum cost** (s.t. **hard** & remaining **soft** clauses are satisfied)
- **Note:** goal is to compute **set** of satisfied (or falsified) clauses; **not** just the cost !

# Many MaxSAT approaches



# Many MaxSAT approaches



- For practical (**industrial**) instances: **core-guided** & **iterative MHS** approaches are the most effective

[MaxSAT14]

Minimal Unsatisfiability

MUS Enumeration

Maximum Satisfiability

Iterative SAT Solving

Core-Guided Algorithms

Minimum Hitting Sets

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

Example CNF formula



## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 12$$

Relax **all** clauses; Set  $UB = 12 + 1$

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 12$$

Formula is SAT; E.g. all  $x_i = 0$  and  $r_1 = r_7 = r_9 = 1$  (i.e. cost = 3)

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 2$$

Refine  $UB = 3$

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 2$$

Formula is SAT; E.g.  $x_1 = x_2 = 1$ ;  $x_3 = \dots = x_8 = 0$  and  $r_4 = r_9 = 1$  (i.e. cost = 2)

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 1$$

Refine  $UB = 2$

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 1$$

Formula is **UNSAT**; terminate

## Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 1$$

MaxSAT solution is last satisfied UB:  $UB = 2$

# Basic MaxSAT with iterative SAT solving

$$x_6 \vee x_2 \vee r_1$$

$$\neg x_6 \vee x_2 \vee r_2$$

$$\neg x_2 \vee x_1 \vee r_3$$

$$\neg x_1 \vee r_4$$

$$\neg x_6 \vee x_8 \vee r_5$$

$$x_6 \vee \neg x_8 \vee r_6$$

$$x_2 \vee x_4 \vee r_7$$

$$\neg x_4 \vee x_5 \vee r_8$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_{11}$$

$$\neg x_3 \vee r_{12}$$

$$\sum_{i=1}^{12} r_i \leq 1$$

MaxSAT solution is last satisfied UB:  $UB = 2$

AtMostk/PB constraints over  
**all** relaxation variables

**All** (possibly many)  
soft clauses relaxed



Minimal Unsatisfiability

MUS Enumeration

**Maximum Satisfiability**

Iterative SAT Solving

Core-Guided Algorithms

Minimum Hitting Sets

# MSU3 core-guided algorithm

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

Example CNF formula

# MSU3 core-guided algorithm

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_2 \vee x_1$$

$$\neg x_1$$

$$x_2 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$\neg x_5 \vee x_3$$

$$\neg x_3$$

Formula is **UNSAT**;  $OPT \leq |\varphi| - 1$ ; Get unsat core

# MSU3 core-guided algorithm

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^6 r_i \leq 1$$

Add relaxation variables and AtMost $k$ ,  $k = 1$ , constraint

# MSU3 core-guided algorithm

$$x_6 \vee x_2$$

$$\neg x_6 \vee x_2$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5$$

$$\neg x_7 \vee x_5$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^6 r_i \leq 1$$

Formula is (again) **UNSAT**;  $OPT \leq |\varphi| - 2$ ; Get unsat core

# MSU3 core-guided algorithm

$$x_6 \vee x_2 \vee r_7$$

$$\neg x_6 \vee x_2 \vee r_8$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^{10} r_i \leq 2$$

Add new relaxation variables and update AtMost $k$ ,  $k=2$ , constraint

# MSU3 core-guided algorithm

$$x_6 \vee x_2 \vee r_7$$

$$\neg x_6 \vee x_2 \vee r_8$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^{10} r_i \leq 2$$

Instance is now SAT

# MSU3 core-guided algorithm

$$x_6 \vee x_2 \vee r_7$$

$$\neg x_6 \vee x_2 \vee r_8$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^{10} r_i \leq 2$$

MaxSAT solution is  $|\varphi| - \mathcal{I} = 12 - 2 = 10$



# MSU3 core-guided algorithm

$$x_6 \vee x_2 \vee r_7$$

$$\neg x_6 \vee x_2 \vee r_8$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^{10} r_i \leq 2$$

MaxSAT solution is  $|\varphi| - \mathcal{I} = 12 - 2 = 10$

AtMostk/PB  
constraints used

Relaxed soft clauses  
become **hard**

# MSU3 core-guided algorithm

$$x_6 \vee x_2 \vee r_7$$

$$\neg x_6 \vee x_2 \vee r_8$$

$$\neg x_2 \vee x_1 \vee r_1$$

$$\neg x_1 \vee r_2$$

$$\neg x_6 \vee x_8$$

$$x_6 \vee \neg x_8$$

$$x_2 \vee x_4 \vee r_3$$

$$\neg x_4 \vee x_5 \vee r_4$$

$$x_7 \vee x_5 \vee r_9$$

$$\neg x_7 \vee x_5 \vee r_{10}$$

$$\neg x_5 \vee x_3 \vee r_5$$

$$\neg x_3 \vee r_6$$

$$\sum_{i=1}^{10} r_i \leq 2$$

MaxSAT solution is  $|\varphi| - \mathcal{I} = 12 - 2 = 10$

AtMostk/PB  
constraints used

Some clauses  
not relaxed

Relaxed soft clauses  
become **hard**

Minimal Unsatisfiability

MUS Enumeration

**Maximum Satisfiability**

Iterative SAT Solving

Core-Guided Algorithms

**Minimum Hitting Sets**

# MHS approach for MaxSAT

$$C_1 = X_6 \vee X_2$$

$$C_2 = \neg X_6 \vee X_2$$

$$C_3 = \neg X_2 \vee X_1$$

$$C_4 = \neg X_1$$

$$C_5 = \neg X_6 \vee X_8$$

$$C_6 = X_6 \vee \neg X_8$$

$$C_7 = X_2 \vee X_4$$

$$C_8 = \neg X_4 \vee X_5$$

$$C_9 = X_7 \vee X_5$$

$$C_{10} = \neg X_7 \vee X_5$$

$$C_{11} = \neg X_5 \vee X_3$$

$$C_{12} = \neg X_3$$

$$\mathcal{K} = \emptyset$$

- Find MHS of  $\mathcal{K}$ :

# MHS approach for MaxSAT

$$C_1 = X_6 \vee X_2$$

$$C_2 = \neg X_6 \vee X_2$$

$$C_3 = \neg X_2 \vee X_1$$

$$C_4 = \neg X_1$$

$$C_5 = \neg X_6 \vee X_8$$

$$C_6 = X_6 \vee \neg X_8$$

$$C_7 = X_2 \vee X_4$$

$$C_8 = \neg X_4 \vee X_5$$

$$C_9 = X_7 \vee X_5$$

$$C_{10} = \neg X_7 \vee X_5$$

$$C_{11} = \neg X_5 \vee X_3$$

$$C_{12} = \neg X_3$$

$$\mathcal{K} = \emptyset$$

- Find MHS of  $\mathcal{K}$ :  $\emptyset$

# MHS approach for MaxSAT

$$C_1 = X_6 \vee X_2$$

$$C_2 = \neg X_6 \vee X_2$$

$$C_3 = \neg X_2 \vee X_1$$

$$C_4 = \neg X_1$$

$$C_5 = \neg X_6 \vee X_8$$

$$C_6 = X_6 \vee \neg X_8$$

$$C_7 = X_2 \vee X_4$$

$$C_8 = \neg X_4 \vee X_5$$

$$C_9 = X_7 \vee X_5$$

$$C_{10} = \neg X_7 \vee X_5$$

$$C_{11} = \neg X_5 \vee X_3$$

$$C_{12} = \neg X_3$$

$$\mathcal{K} = \emptyset$$

- Find MHS of  $\mathcal{K}$ :  $\emptyset$
- $\text{SAT}(\mathcal{F} \setminus \emptyset)$ ?

# MHS approach for MaxSAT

$$C_1 = X_6 \vee X_2$$

$$C_2 = \neg X_6 \vee X_2$$

$$C_3 = \neg X_2 \vee X_1$$

$$C_4 = \neg X_1$$

$$C_5 = \neg X_6 \vee X_8$$

$$C_6 = X_6 \vee \neg X_8$$

$$C_7 = X_2 \vee X_4$$

$$C_8 = \neg X_4 \vee X_5$$

$$C_9 = X_7 \vee X_5$$

$$C_{10} = \neg X_7 \vee X_5$$

$$C_{11} = \neg X_5 \vee X_3$$

$$C_{12} = \neg X_3$$

$$\mathcal{K} = \emptyset$$

- Find MHS of  $\mathcal{K}$ :  $\emptyset$
- $\text{SAT}(\mathcal{F} \setminus \emptyset)$ ? **No**

# MHS approach for MaxSAT

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$$c_2 = \neg x_6 \vee x_2$$

$$c_3 = \neg x_2 \vee x_1$$

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$$c_{12} = \neg x_3$$

$$\mathcal{K} = \emptyset$$

- Find MHS of  $\mathcal{K}$ :  $\emptyset$
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- $\text{SAT}(\mathcal{F} \setminus \{c_4, c_9\})$ ? Yes
- Terminate & return 2

# MaxSAT solving with SAT oracles – a sample

- A sample of recent algorithms:

Algorithm	# Oracle Queries	Reference
Linear search SU	Exponential***	[BP10]
Binary search	Linear*	[FM06]
FM/WMSU1/WPM1	Exponential**	[FM06, MP08, MMSP09, ABL09, ABGL12]
WPM2	Exponential**	[ABL10, ABL13]
Bin-Core-Dis	Linear	[HMM11, MHM12]
Iterative MHS	Exponential	[DB11, DB13a, DB13b]

\*  $\mathcal{O}(\log m)$  queries with SAT oracle, for (partial) unweighted MaxSAT

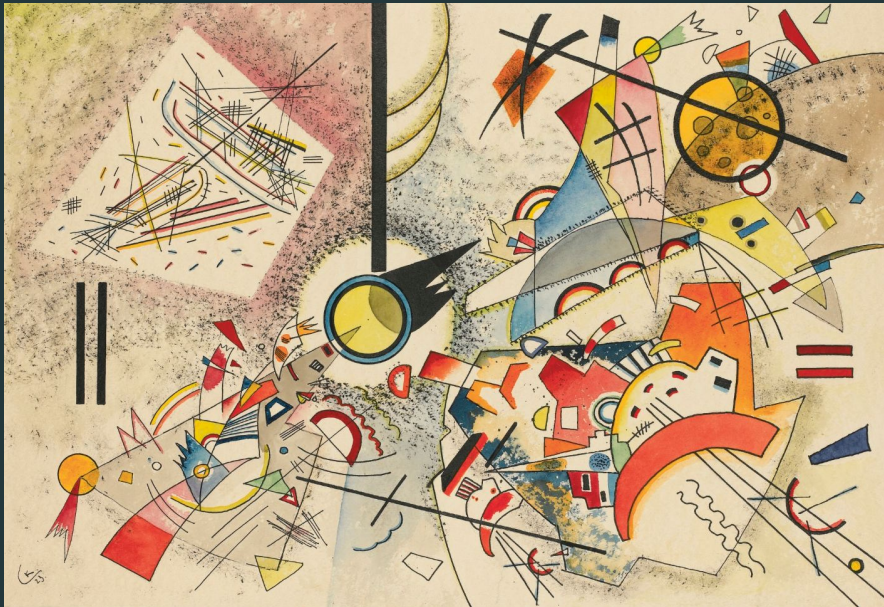
\*\* Weighted case; depends on computed cores

\*\*\* On # bits of problem instance (due to weights)

- But also additional recent work:

- Progression [IMM<sup>+</sup>14]
- Soft cardinality constraints (OLL) [MDM14, MIM14]
  - Recent implementation (RC2, using PySAT) won 2018 MaxSAT Evaluation
- MaxSAT resolution [NB14]
- ...

# 4 Sample of Applications

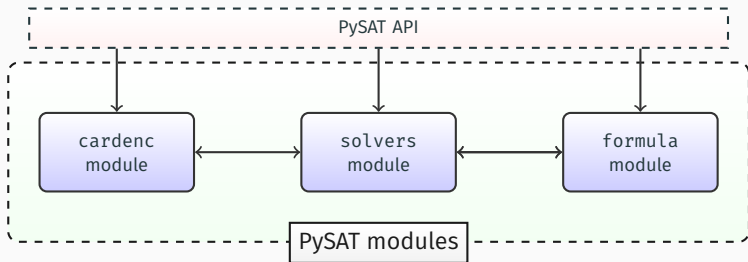


# Flagship applications

- Bounded (& unbounded) model checking
- Automated planning
  
- Software model checking
- Equivalence checking
- Package management
- Design debugging
  
- Haplotyping

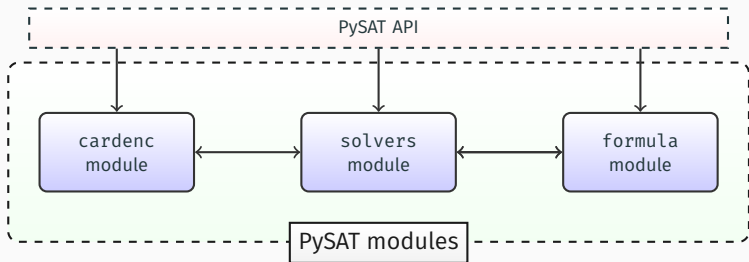
# PySAT – SAT for all

[IMM18]

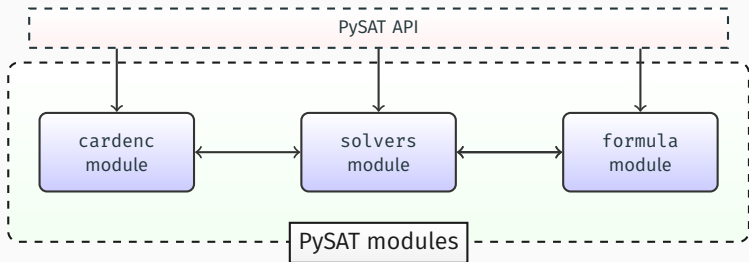


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- Comprehensive list of [SAT solvers](#)
- Comprehensive list of [cardinality encodings](#)
- Fairly comprehensive documentation
- Several use cases



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- Two-level logic minimization with SAT
  - Reimplementation of Quine-McCluskey with SAT oracles

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# Recent applications

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- **Smallest (explainable) decision trees** [NIPM18]
  - Computation of smallest decision trees

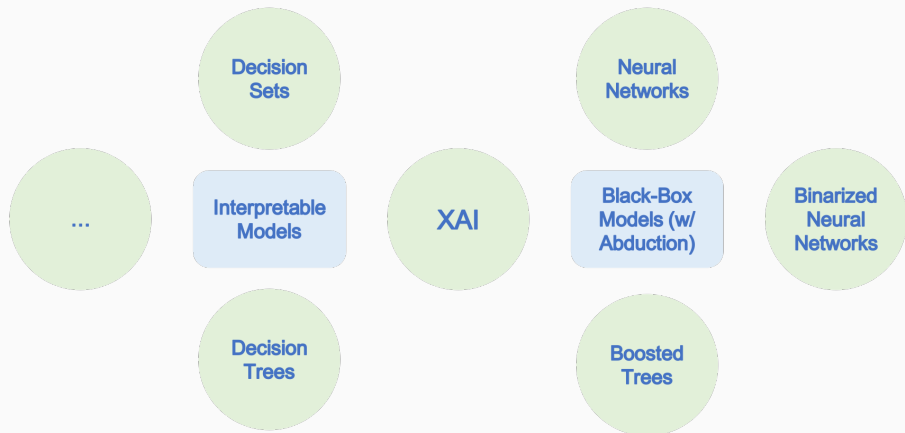
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- Lots of other applications, by us & by others

# SAT (& SMT) meet(s) eXplainable AI



## Smallest **decision trees** – encoding sizes in bytes

[NIPM18]

Model	Weather	Mouse	Cancer	Car	Income
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IJCAI'18	190K	1.2M	5.2M	4.1M	1.2G

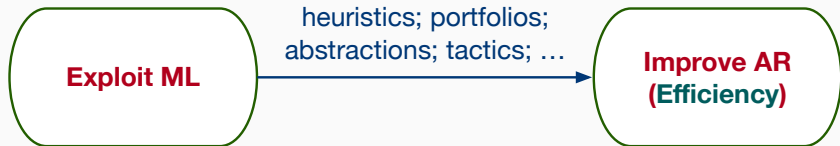


- **Positive:**
  - General approach, applicable to **any** ML model represented as a set of constraints
  - E.g. ability to explain predictions of NNs
- **Negative:**
  - NN sizes are fairly small, i.e. tens of neurons
  - Best results with ILP-based approach
    - SMT/SAT models currently ineffective
    - But, algorithms inspired SAT-based solutions

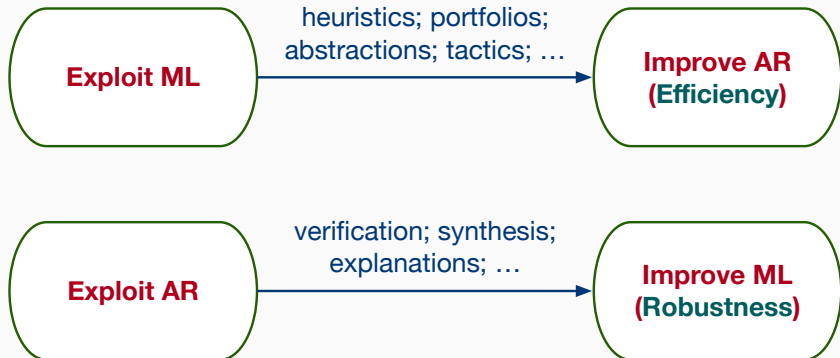
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- So, where is **SAT** used?
  - Computing primes, with **SAT**-inspired algorithms
    - In general, oracle-based problem solving
  - Modeling NNs & boosted trees with **SMT**
  - Modeling BNNs with **SAT**

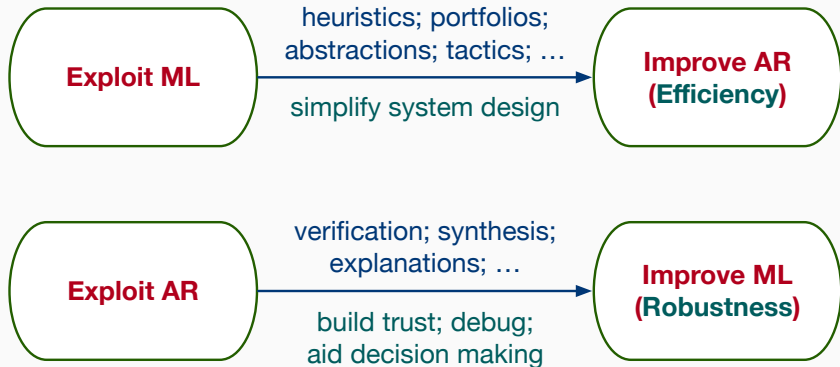
## Machine learning vs. automated reasoning



# Machine learning vs. automated reasoning



# Machine learning vs. automated reasoning





# Is there a future for SAT?



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- SAT-based problem solving is here to stay
  - And with high-profile applications, e.g. XAI
- Novel modular reasoning insights are the part of the future
  - **A prediction:**  
The future will see widespread uses of SAT-enabled modular reasoners

# Questions?



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